

AMENDMENTS TO THE SPECIFICATION

In the Specification:

Please replace the paragraph beginning on page 5, line 3, with the following paragraph:

A selection signal corresponding to a predetermined channel output from the third multiplexer 170 110 is input as a control signal for the first multiplexer 130. The first multiplexer 130 outputs a logical address of a predetermined channel in the LAT 120 according to the selection signal corresponding to the predetermined channel. When symbol data corresponding to the predetermined channel are stored in the buffer memory 160, the logical address includes logical address information mapped to a physical address of a corresponding segment in which the symbol data are stored.